

RANDOM ACCESS MEMORY INITIALIZATION

ABSTRACT

A memory has a set of address spaces to which token data is written and read.

- 5 Each address space has a token status bit. A token generator allocates token data to the memory address spaces. Upon a reset occurring, a logic circuit provides logic “0” to the token generator disabling status bit checking control so that all the tokens can be issued sequentially. New token data is allocated to the address spaces sequentially and the respective status bit is updated or maintained as logic “1”. When all address spaces have
- 10 been allocated, the logic circuit provides the actual state of the status bit to the token generator to control subsequent allocations.